

DEPARTMENT OF ELECTRONICS ENGINEERING SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ़ टेक्नोलॉजी, सूरत

क्रमांक:DoECE/Mid-Sem/ 50न

/2025-26

दिनांक:27-10-2025

Mid-Semester Exam Schedule (November-2025)

B. Tech. I – Electronics & Communication Engineering (1st Semester)

Exam Date	Time (04:15 PM to 05:45 PM)			
03-11-2025	Semiconductor Physics and Devices(EC101)			
04-11-2025 English and Professional Communication(HS110)				
06-11-2025	Fundamentals of Computer and Programming(EC1			
07-11-2025				
08-11-2025 Fundamentals of Electrical Engineering(EE110)				

B. Tech. I - Electronics and VLSI Engineering

(1st Semester)

Exam Date	Time (04:15 PM to 05:45 PM)				
03-11-2025 Semiconductor Physics and Devices(VL101)					
04-11-2025 English and Professional Communication(HS1					
06-11-2025	Fundamentals of Computer and Programming(VL103)				
07-11-2025 Mathematics-I (MA117)					
08-11-2025	Fundamentals of Electrical Engineering(EE110)				

M. Tech. I & Ph.D. - Electronics & Communication Engineering Communication Systems (1st Semester)

Exam Date	Time (04:15 PM to 05:45 PM)			
03-11-2025	RF Circuits & Systems (ECCS101)			
04-11-2025 Advance Optical Communication Systems (ECC				
06-11-2025	Advance DSP (ECCS105)			
07-11-2025 Information Theory & Coding (ECCS111)				
08-11-2025 Digital VLSI Design (ECVL101)				

M. Tech. I & Ph.D. – Electronics & Communication Engineering VLSI & Embedded Systems (1st Semester)

Exam Date	Time (04:15 PM to 05:45 PM)			
03-11-2025	MOS Devices and Technology (ECVL103)			
04-11-2025	Testing and Verification of VLSI Circuits (ECVL117)			
06-11-2025	Semiconductor IC Technology (ECVL111)			
07-11-2025	Embedded Systems (ECVL105)			
08-11-2025	Digital VLSI Design (ECVL101)			

Note: -

- a) Students has to report 15 minutes before the commencement of the respective examination.
- b) Exchange of writing materials, stencils, mathematic instruments etc. are strictly prohibited. Carrying Pager, Digital Diary, Cellular Mobile phone, Programmable calculator, Smart watch and other electronic Gadget in the examination hall is also prohibited. Any student found with prohibited materials, disciplinary action will be initiated against the student.
- c) Do not bring any book, notes or scribbling paper into the examination hall. Do not speak or communicate in any other way and do not take any answer book whiling leaving the examination hall otherwise disciplinary action will be initiated.
- d) Students will not be allowed to go outside the Classroom for the whole duration of the exam.

शिल्पी गुरी

Copy to:

- 1) Dy. Registrar (Acad) 2) Chief Exam Coordinator 3) Student NB DoECE
- 4) upload@svnit.ac.in 5) All concerned Departments

In case any query, contact Examination Coordinators, DoECE



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Mid-Semester Exam (November 2025)

Seating Arrangement:

B. Tech – I	EC022N	EC021N	EC222N	EC203N	EC106N	EC105N
(All	U25EV001-	U25EV037-	U25EC001-	U25EC036-	U25EC076-	U25EC116-
Subjects)	U25EV036 (36)	U25EV063 (27)	U25EC035 (35)	U25EC075 (40)	U25EC115 (40)	U25EC145 (30)
	EC022N	EC021N		•		
M. Tech – I	VLSI	CS				
	(All	(All				
	Students)	Students)				

Examination Coordinator, DoECE

HOD, Doece 1 10125